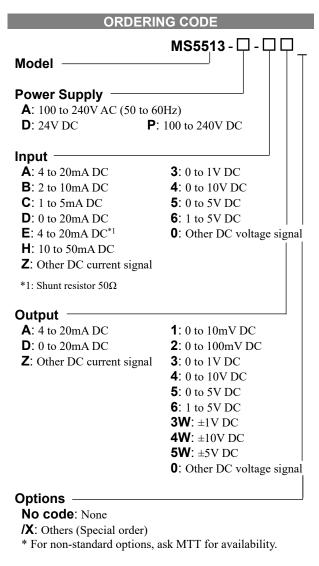


## DESCRIPTION

The MS5513 is a plug-in square-root extractor that extracts the square roots of DC current or voltage signals, converts them into commonly used DC signals and provides an isolated single output.

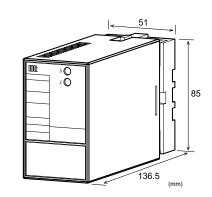


## ORDERING INFORMATION

To place an order, please use the ordering code format as shown above. (e.g.) MS5513-A-6A

(e.g.) Meee 10-7 (-6) (

Other Ordering Examples: For an input code of "0": MS5513-D-04 (Input: 2 to 5V) For an output code of "Z": MS5513-A-EZ (Output: 8 to 20mA)



#### **SPECIFICATIONS**

POWER SECT	ION		
Power	100 to 240V AC: 85 to 264V AC (47		
Requirement	to 63Hz)		
•	24V DC: 24V D	C±10%	
	100 to 240V DO	C: 85 to 264V DC	
Power Sensitivity	Better than $\pm 0.1\%$ of span for each		
	power supply ra	-	
Power Line Fuse	160mA fuse		
Maximum Power C	onsumption		
Power 10	0-240VAC 24	V DC 100-240V DC	
	Approx. Ap	prox. Approx.	
	5.5VA 1	.6W 6.0W	
Input Resistance			
Voltage Input (DC)	$1 M\Omega$ min. with	or without power.	
Current Input (DC)	4 to 20mA (std	.) 250Ω	
	2 to 10mA	250Ω	
	1 to 5 mA	100Ω	
	0 to 20mA	50Ω	
	10 to 50mA	10Ω	
Allowable Input Vol	tage		
Voltage Input Model	30V DC max., c	ontinuous. (Standard	
	for a span up to	10V)	
Current Input Model	40mA DC max., continuous.		
	(Standard for 4	to 20mA)	
Ranges Available		,	
0	Current Signal	Voltage Signal	
Input Range (DC)	0 to 100mA	0 to 300V	
Input Span (DC)	100µA to 100m.	A 200mV to 300V	
Input Bias	0 to 100%	0 to 100%	
Input Spec. Ex. 1: For 4 to 20mA input, the input span is			
16mA and the bias +25%.			
Input Spec. Ex. 2: For 2 to 6V input, the input span is 4V			
and the bias $+50\%$ .			

OUTPUT SEC	TION			
Allowable Output Load				
Voltage Output (DC)	1V span and up	2mA max.		
	10mV	$10k\Omega$ min.		
	100mV	$100k\Omega$ min.		
Current Output (DC)	4 to 20mA	750Ω max.		
Zero Adjustment	Approx. ±5% of spa	ın.		
	(Adjustable by the f			
	trimmer.)			
Span Adjustment	Approx. ±5% of span.			
1 5	(Adjustable by the front-accessible			
	trimmer.)			
Square-Root	$X = 10 \times \sqrt{Y}$			
Extraction	where			
	X = Output signal (	) to 100%)		
	Y = Input signal (0)			
	Note: The cutoff fur			
	when the outp	ut is less than or		
	equal to 8%±1			
Ranges Available				
0	Current Signal	Voltage Signal		
Output Range (DC)	0 to 20mA	-10 to 10V		
Output Span (DC)	4 to 20mA	10mV to 20V		
Output Bias	0 to 100%	-100 to 100%		
* For current output	* For current output signals, the accuracy of any current			
output smaller than	0.1mA is not guarant	eed.		
Output Spec. Ex. 1: I	For 4 to 20mA output,	the output span		
	s 16mA and the bias +			
	For -1 to 4V output, th	e output span is		
5	V and the bias -20%.			
PERFORMAN	CE			
Accuracy Rating	Better than $\pm 0.2\%$ c	of span (at		
, ,	25°C±5°C).	•		
Temperature	Better than $\pm 0.2\%$ c	of span per 10°C		
Effect	change in ambient.			
Response Time	120ms max. (0 to 90	0%) with a step		
•	input at 100%.	· •		
CMRR	100dB min. (500V AC, 50/60Hz)			
Isolation	3-way isolation between input,			
	output, and power.	* '		
Inculation	100MO min (@ 500)	WDC) hatriaan		

 $100M\Omega$  min. (@ 500V DC) between

Input / Output / Power: 2000V AC for 1 minute (Cutoff current: 0.5mA)

Ambient temperature: -5 to 55°C

(non-condensing)

input, output, and power.

Tested as per ANSI/IEEE

Humidity: 5 to 90% RH

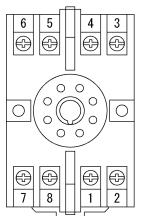
C37.90.1-1989.

-10 to 60°C

PHYSICAL		
Installation	Wall/DIN rail mounting	
Mounting	Vertical	
Orientation		
Screwing Torque	0.78 to 1.18 [Nm] * Recommended	
Wiring	M3.5 screw terminal connection	
External	W51 × H85 × D136.5mm	
Dimensions	(including the socket)	
Weight	Main unit: 200g max.	
-	Socket: 60g max.	
Housing	ABS resin (UL 94V-0)	
Socket	ABS resin (UL 94V-0)	
Screw Terminal	Galvanized steel with trivalent	
	chromate finish	
Printed Circuit	Glass fabric epoxy resin	
Board	(FR-4: UL 94V-0)	
Conformal	HumiSeal <sup>®</sup> 1A27NS (Polyurethane)	
Coating		
_		

\* HumiSeal® is a registered trademark of Chase Corporation.

# TERMINAL ASSIGNMENT



$\bigcirc$	+ OUTPUT	
2	- OUTPUT	
3	+ INPUT	
4	- INPUT	
5	N.C.	
6	N.C.	
$\bigcirc$	P (+) POWER	
8	N (-)	

Insulation Resistance

Dielectric

Strength Surge Withstand

Capability

Operating

Storage

Environment

Temperature

### **BLOCK DIAGRAM**

